

FIG.1

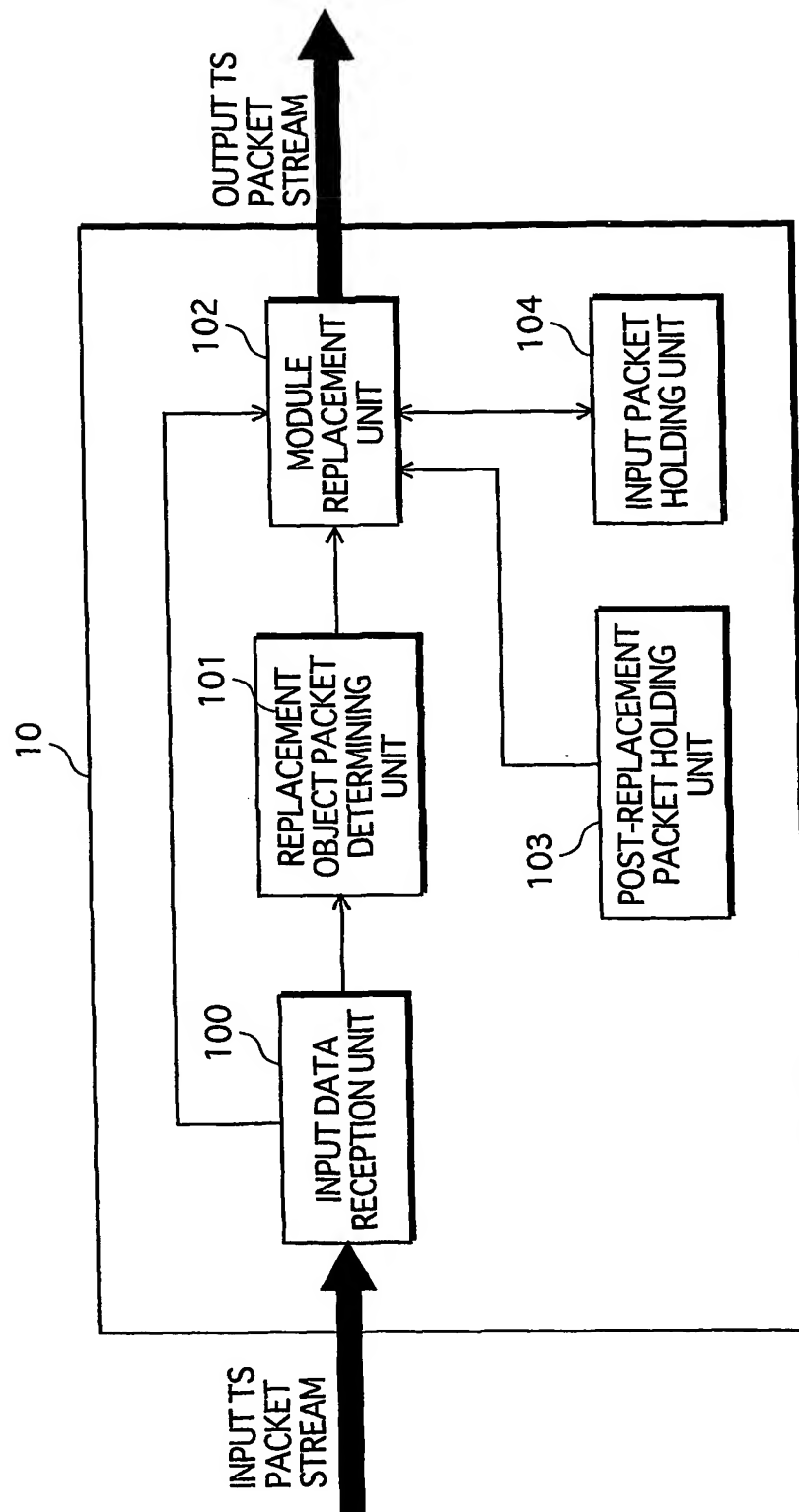


FIG.2

200	201	202	203	204	205	206	207	208	209	210	211	212	213	214	215	216
PID=100 mid=0 snum=2 bnum=0 pnum=3	PID=101 mid=0 snum=5 bnum=0 pnum=4	PID=100 mid=0 snum=5 bnum=0 pnum=4	PID=100 mid=0 snum=5 bnum=0 pnum=4	PID=101 mid=0 snum=5 bnum=0 pnum=4	PID=100 mid=0 snum=2 bnum=1 pnum=4	PID=101 mid=0 snum=5 bnum=1 pnum=2	PID=100 mid=0 snum=5 bnum=1 pnum=2	PID=100 mid=0 snum=5 bnum=1 pnum=2	PID=101 mid=0 snum=5 bnum=1 pnum=2	PID=100 mid=0 snum=5 bnum=1 pnum=2	PID=101 mid=0 snum=5 bnum=1 pnum=2	PID=100 mid=1 snum=10 bnum=0 pnum=4	PID=100 mid=1 snum=10 bnum=0 pnum=4	PID=101 mid=1 snum=10 bnum=0 pnum=4	PID=100 mid=1 snum=10 bnum=0 pnum=4	PID=100 mid=1 snum=10 bnum=0 pnum=4

FIG.3

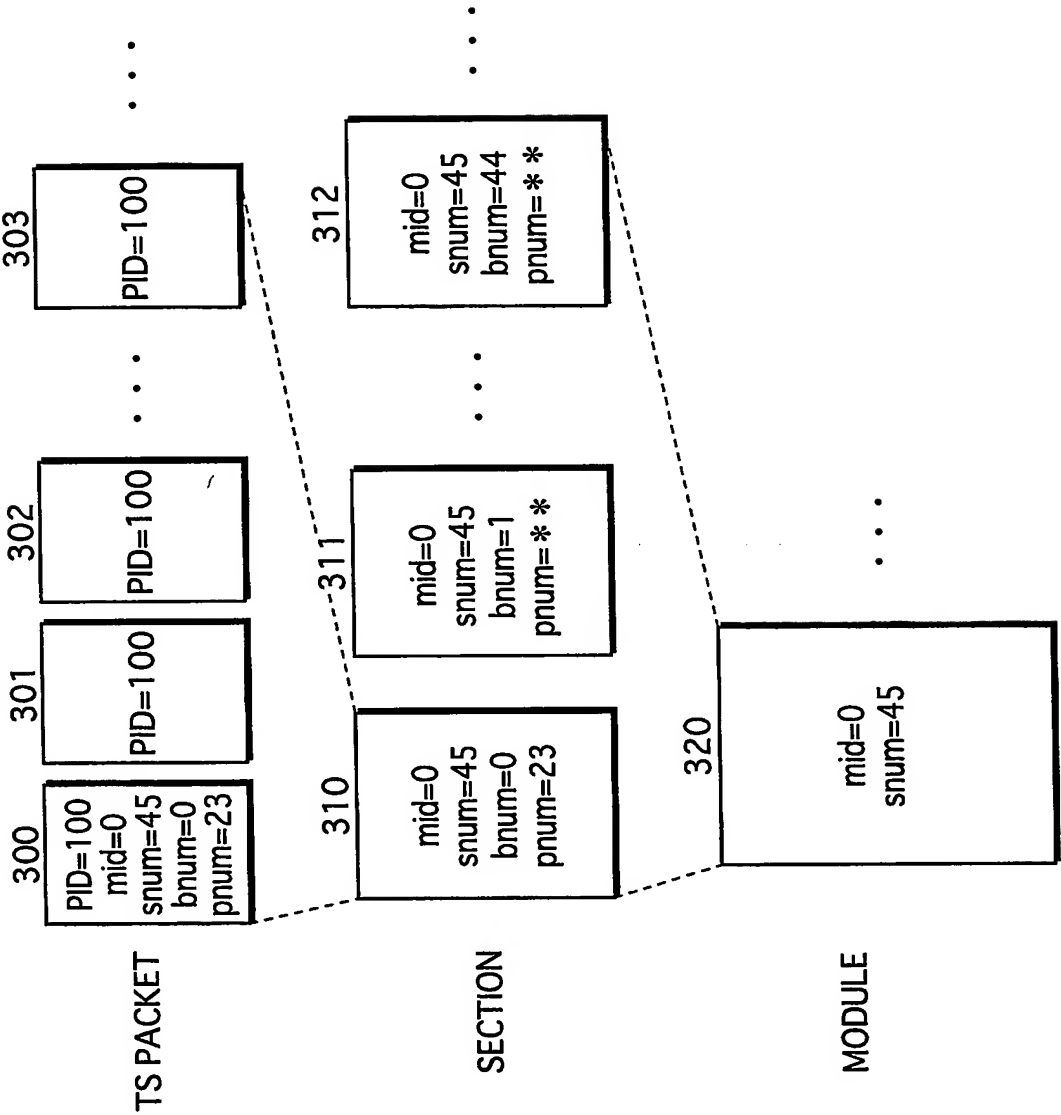


FIG. 4

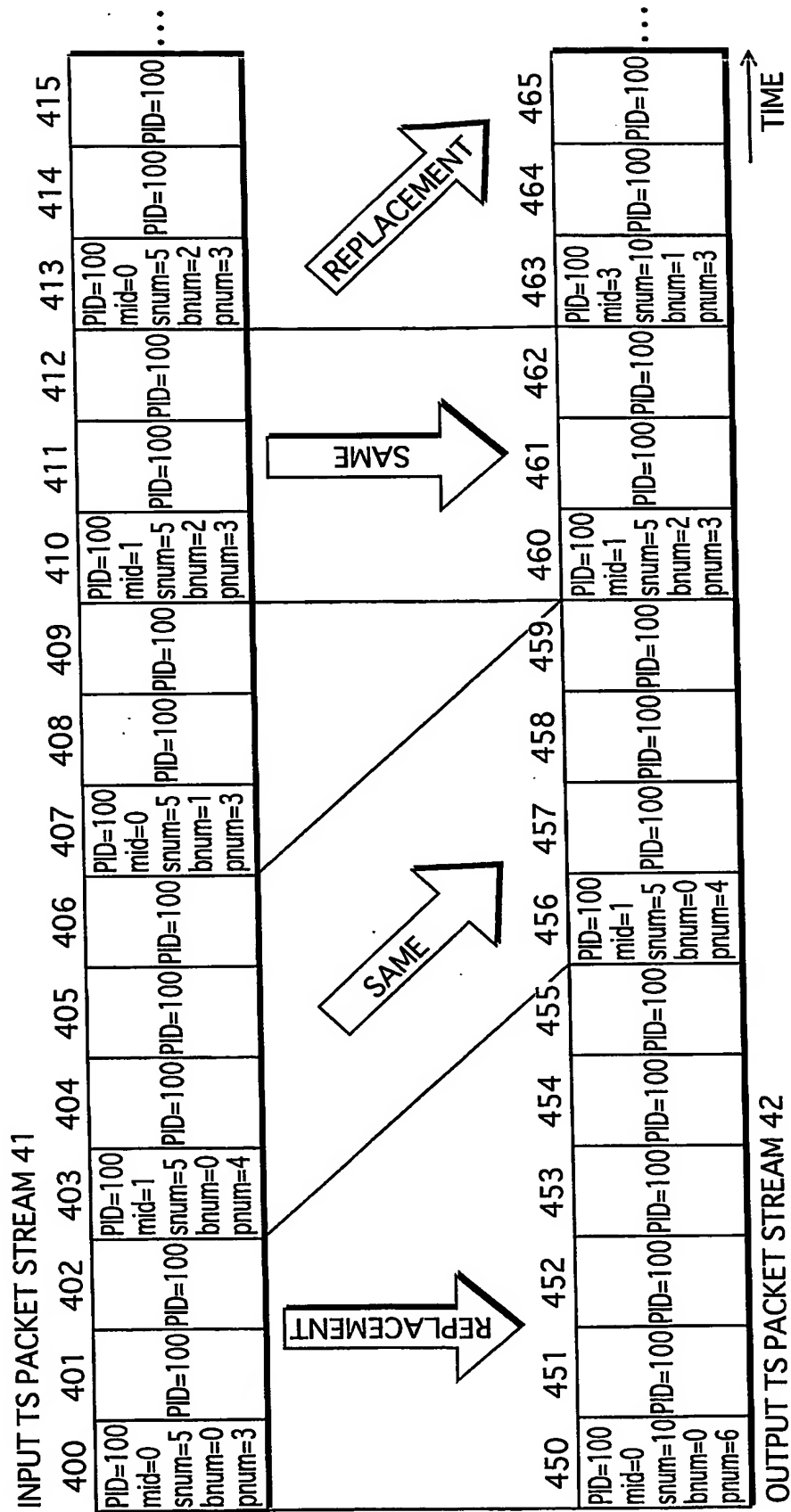


FIG.5

POST-REPLACEMENT MODULE 50

SECTION IDENTIFIED AS BLOCK NUMBER = 0

500	501	502	503	504	505
PID=100 mid=0 snum=10 bnum=0 pnum=6					

SECTION IDENTIFIED AS BLOCK NUMBER = 1

510	511	512
PID=100 mid=0 snum=10 bnum=1 pnum=3		

SECTION IDENTIFIED AS BLOCK NUMBER = 2

520	521	522	523	524
PID=100 mid=0 snum=10 bnum=2 pnum=5				

⋮

SECTION IDENTIFIED AS BLOCK NUMBER = 9

530	531	532	533
PID=100 mid=0 snum=10 bnum=9 pnum=4			

FIG. 6A

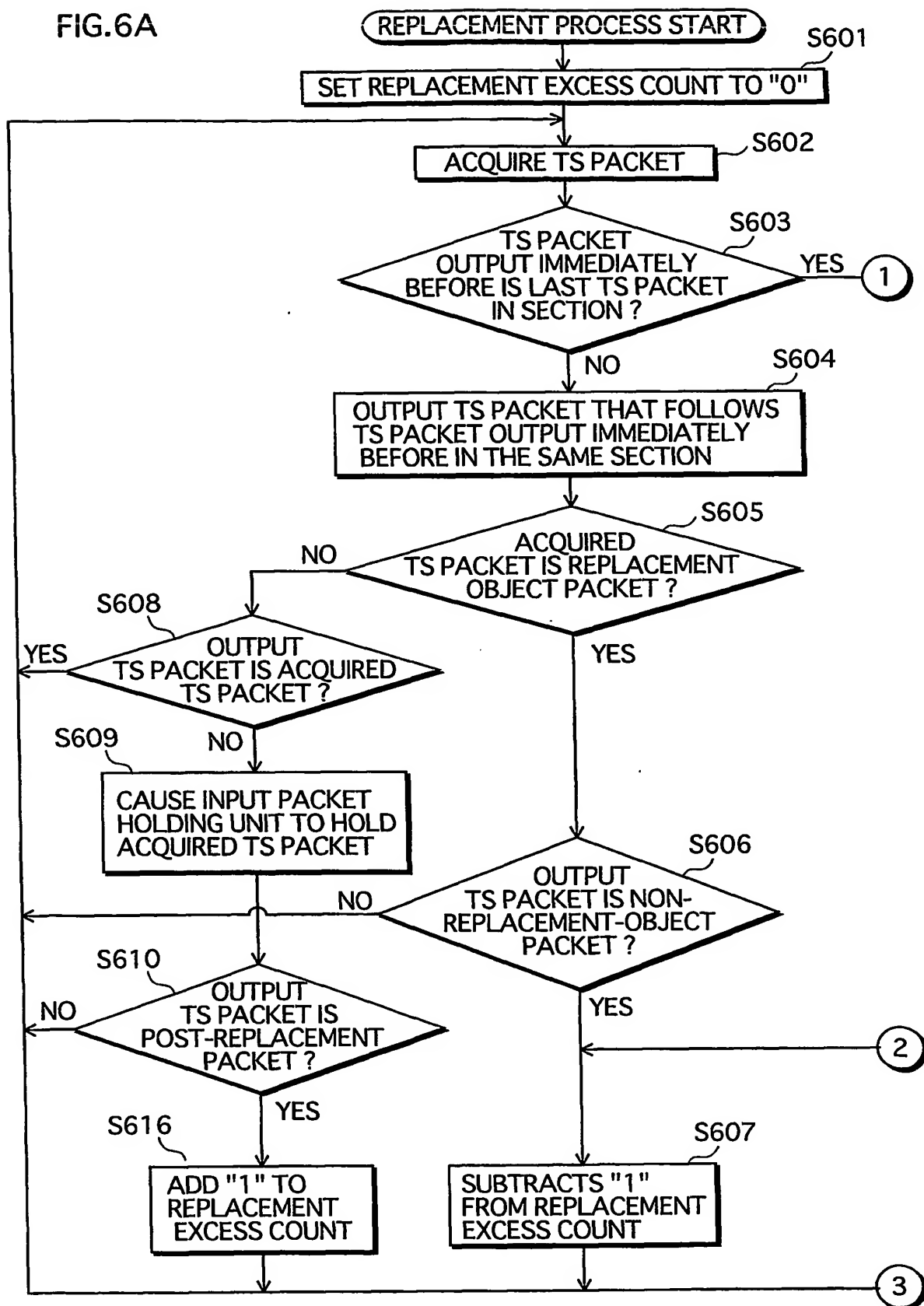


FIG.6B

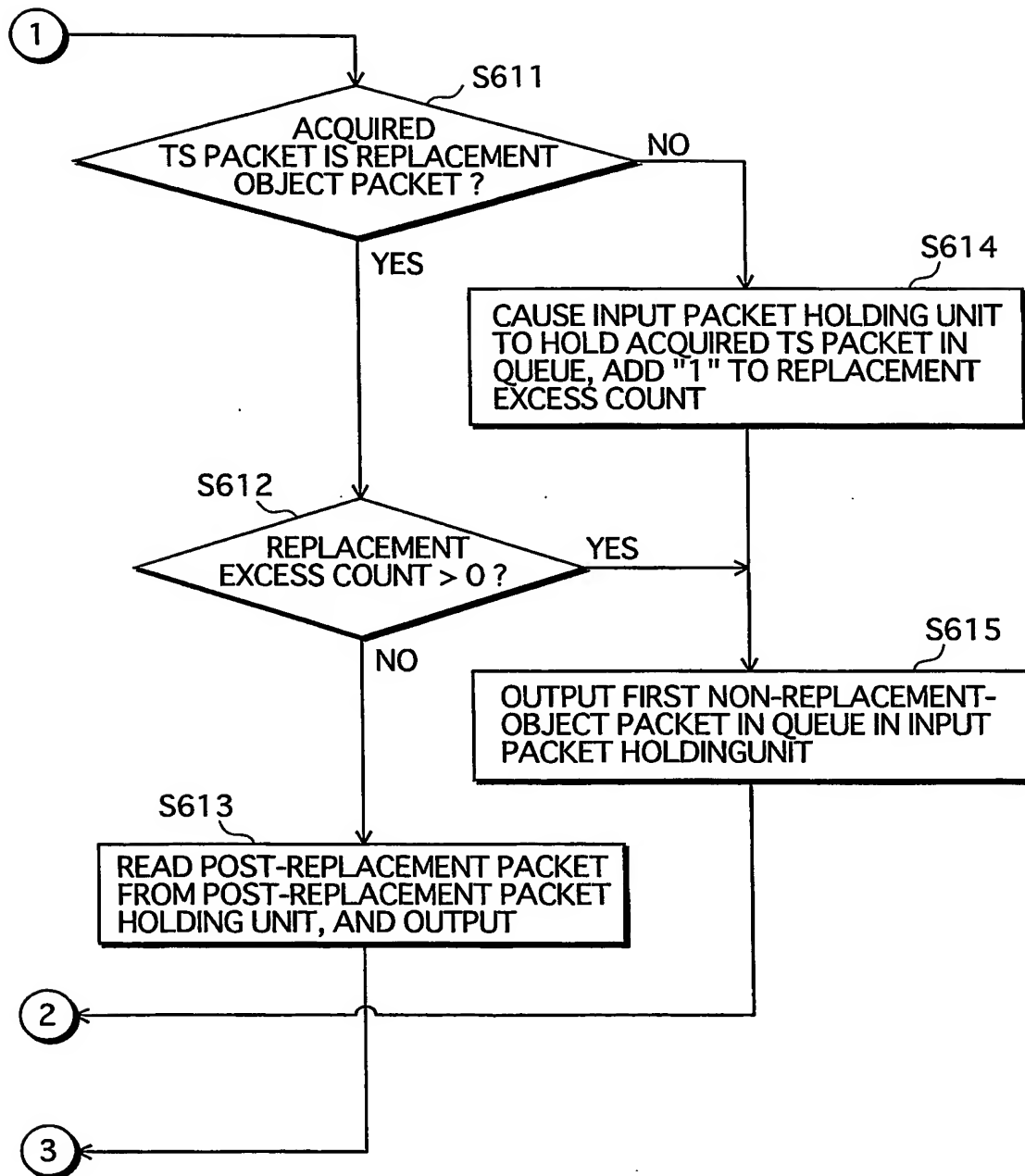


FIG.7

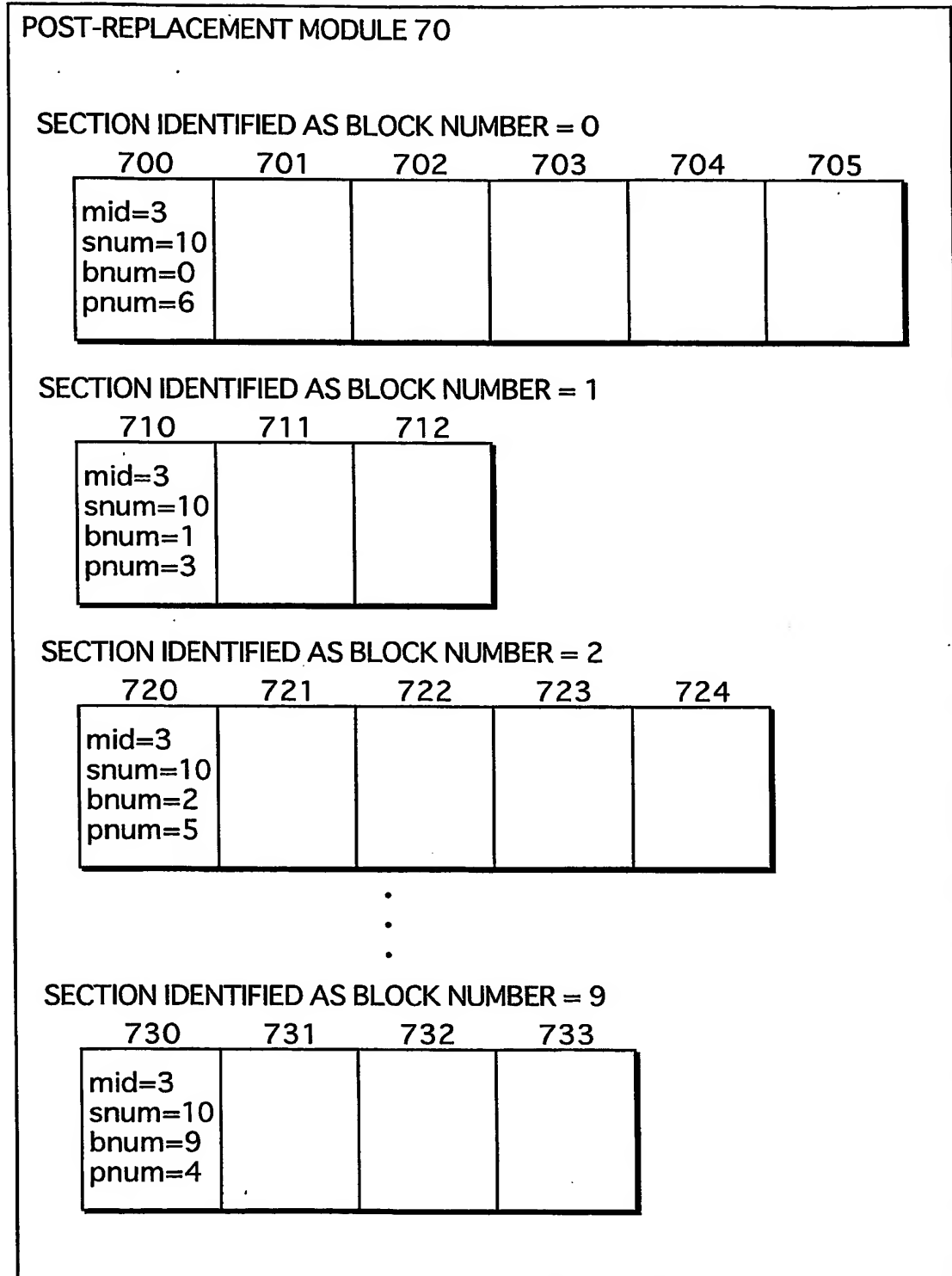


FIG.8

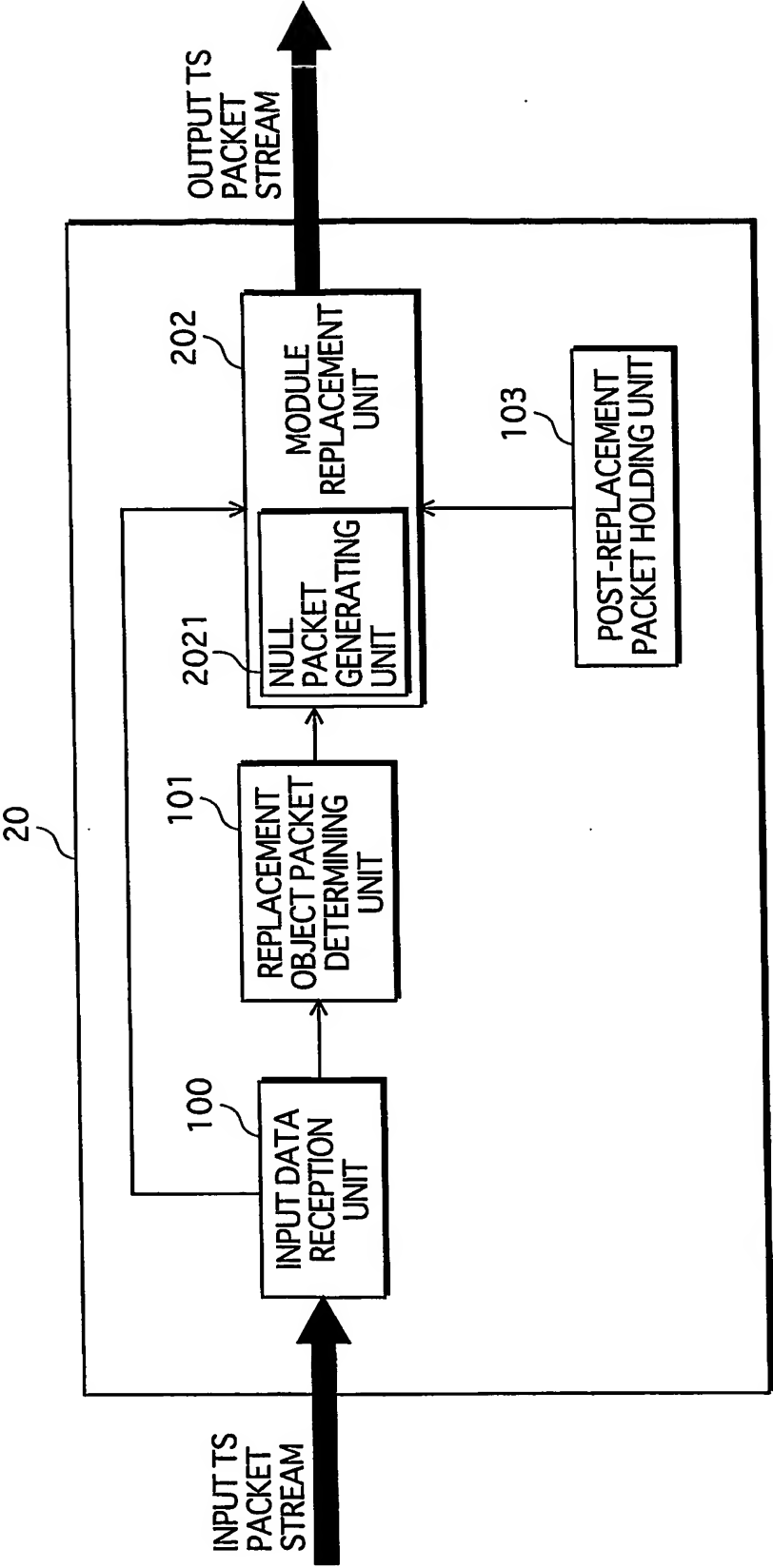


FIG.9

POST-REPLACEMENT MODULE 90

SECTION 91 IDENTIFIED AS BLOCK NUMBER = 0

900	901	902	903
mid=0 snum=5 bnum=0 pnum=4			

SECTION 92 IDENTIFIED AS BLOCK NUMBER = 1

904	905	906	907
mid=0 snum=5 bnum=1 pnum=4			

SECTION 93 IDENTIFIED AS BLOCK NUMBER = 2

908	909	910	911
mid=0 snum=5 bnum=2 pnum=4			

SECTION 94 IDENTIFIED AS BLOCK NUMBER = 3

912	913	914	915
mid=0 snum=5 bnum=3 pnum=4			

SECTION 95 IDENTIFIED AS BLOCK NUMBER = 4

916	917
mid=0 snum=5 bnum=4 pnum=2	

FIG.10

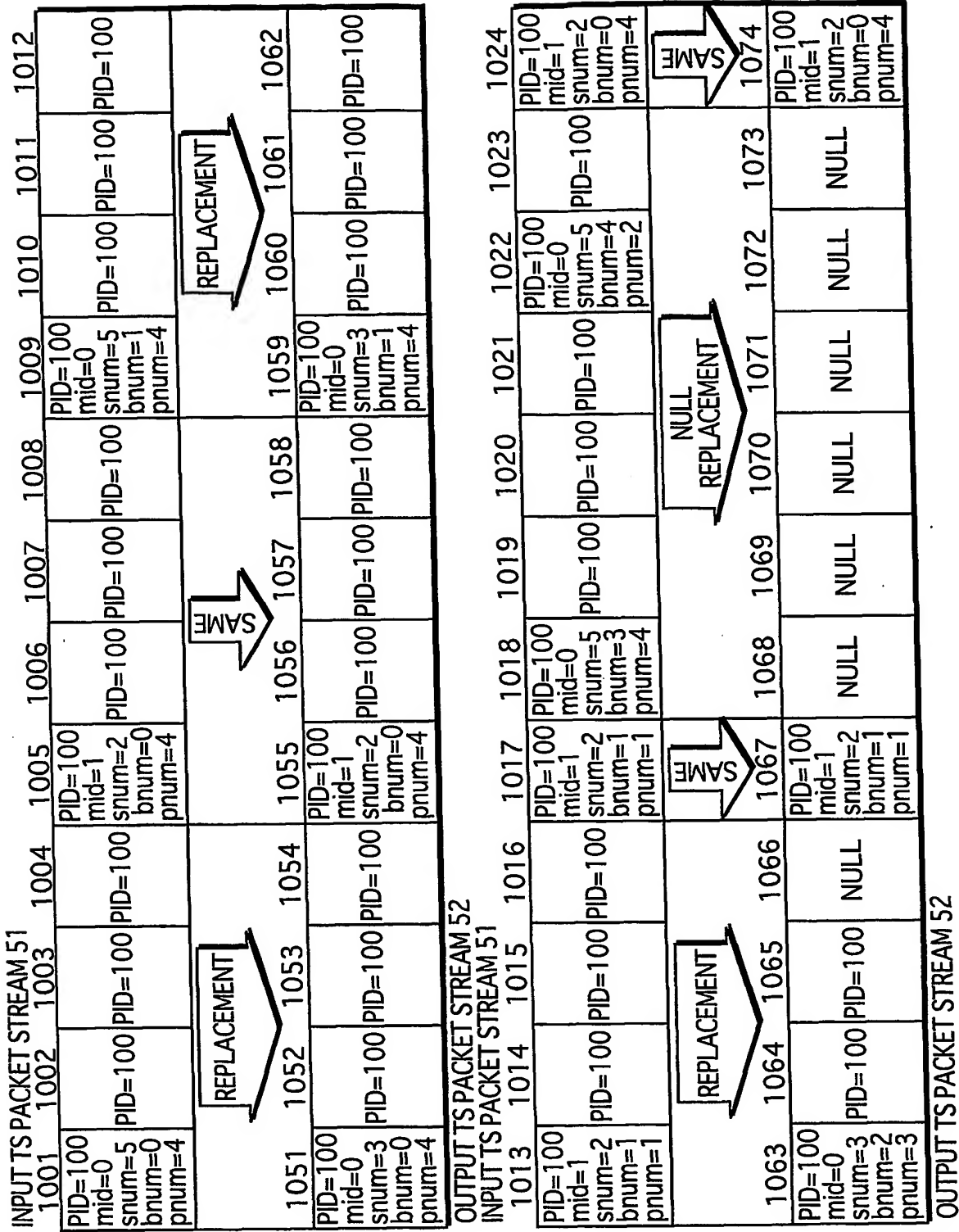


FIG.11

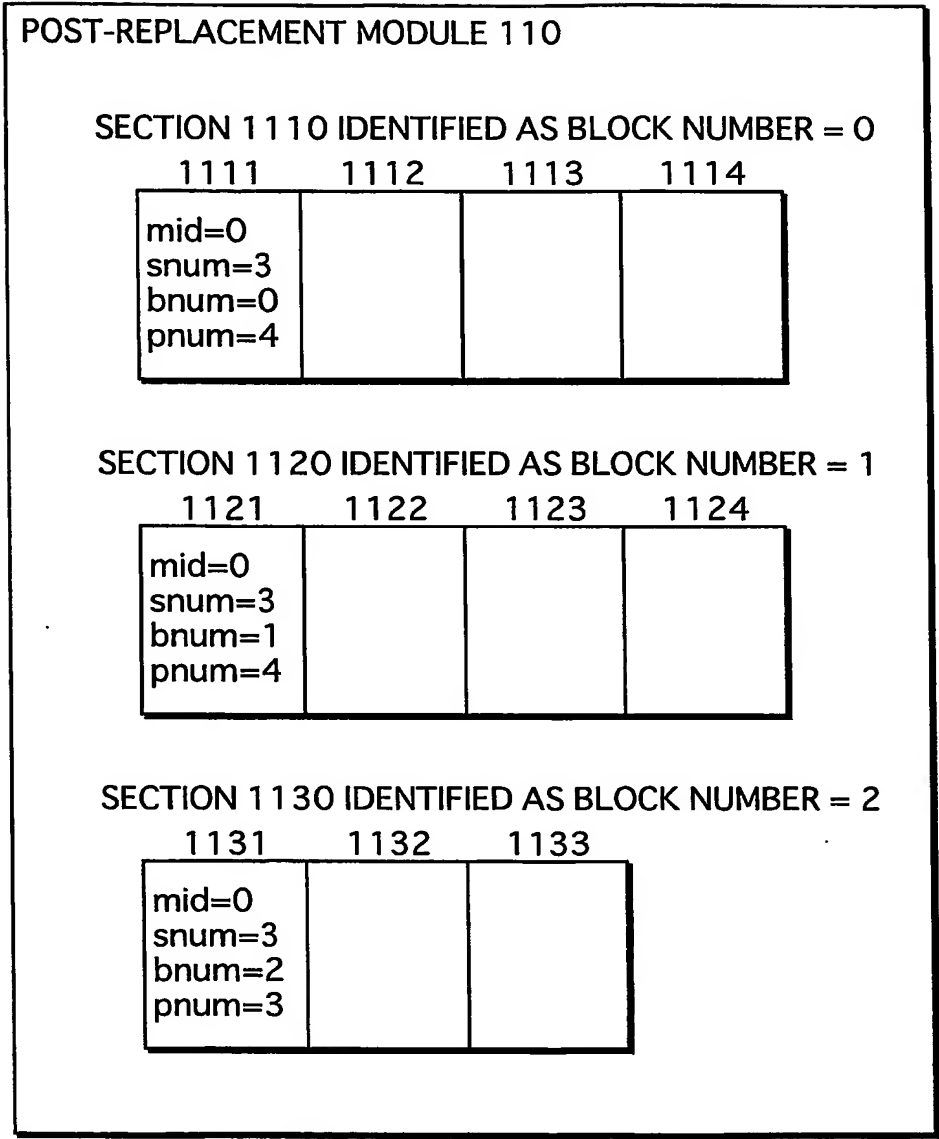


FIG.12

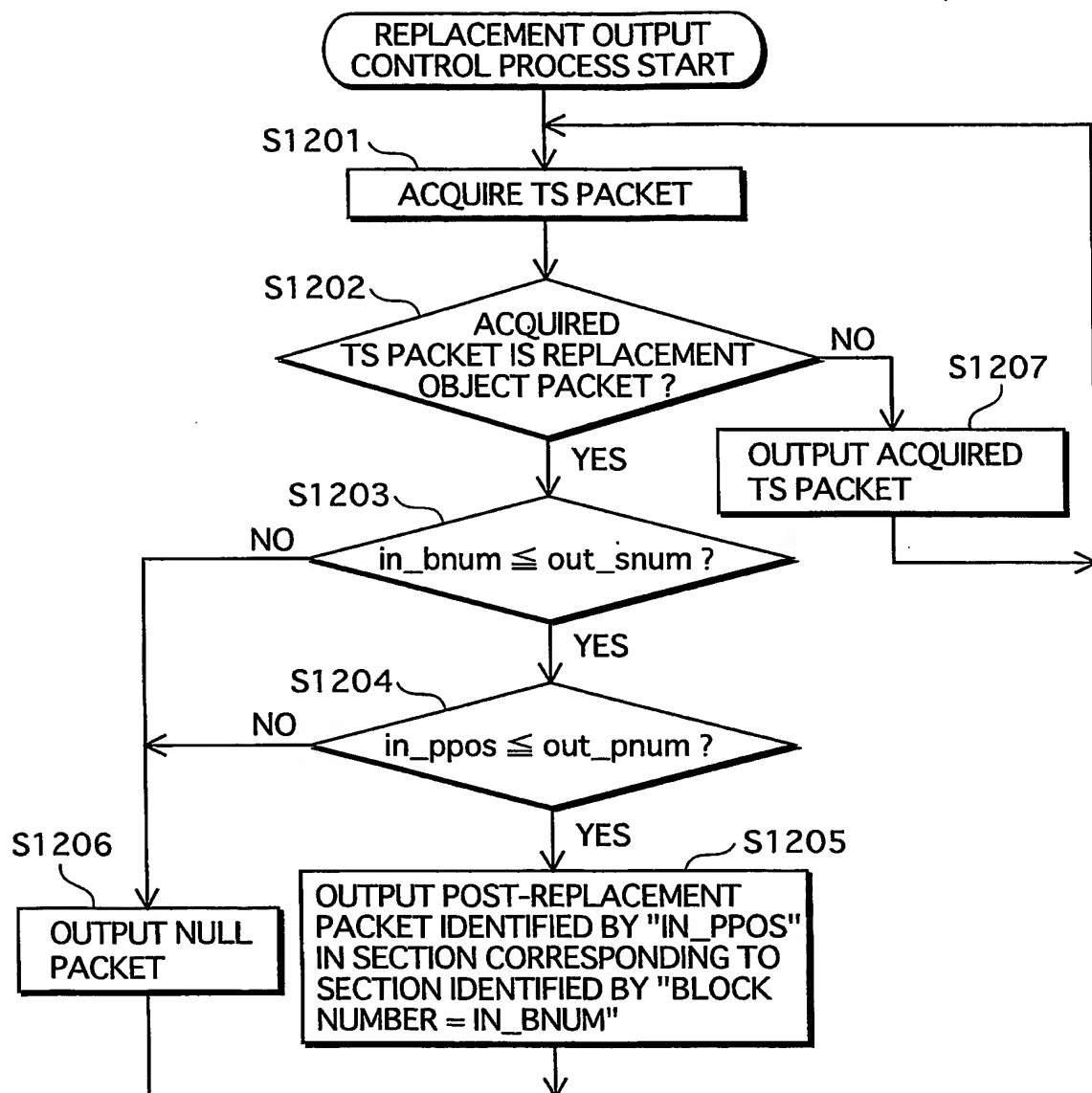
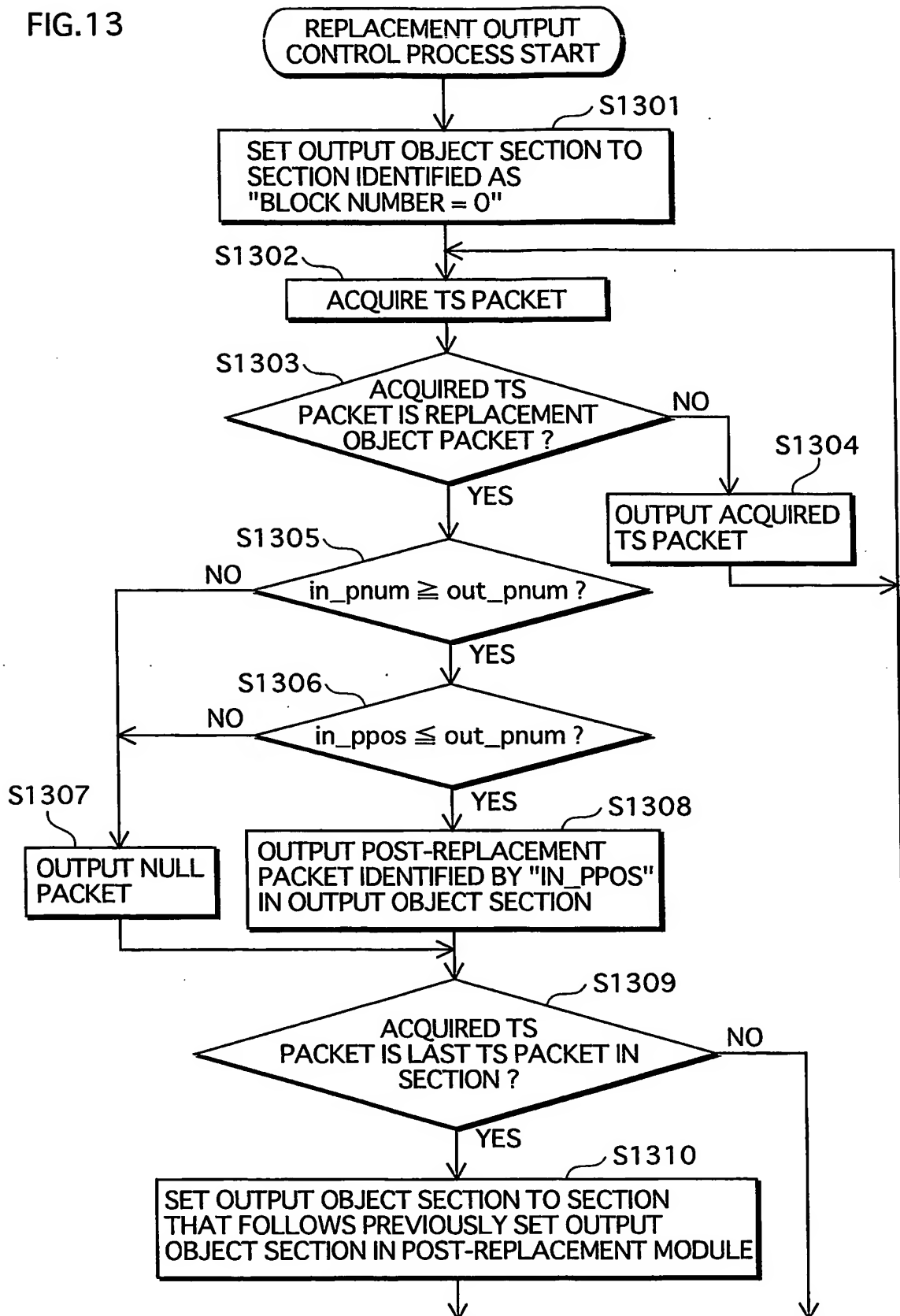


FIG.13



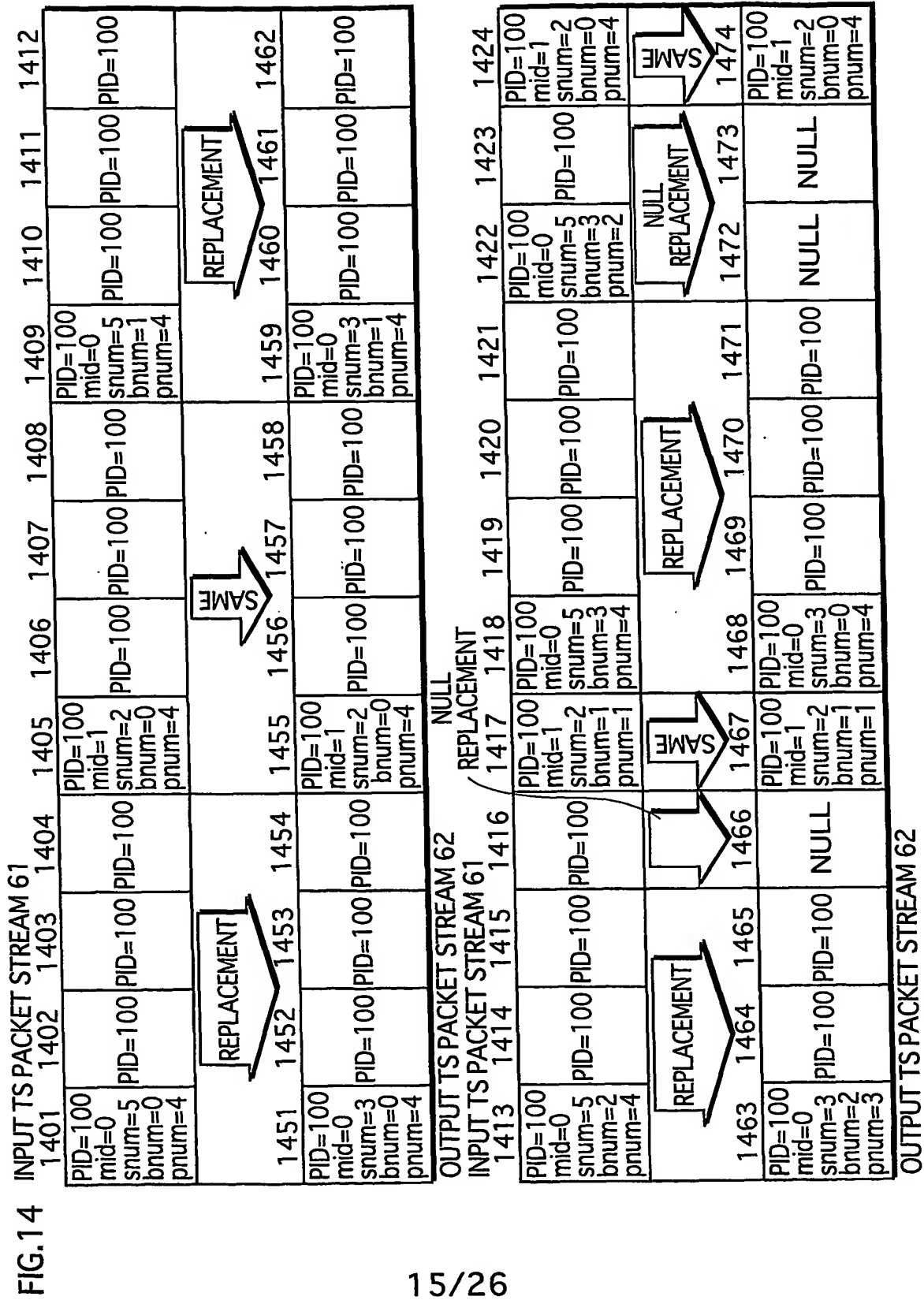


FIG.15

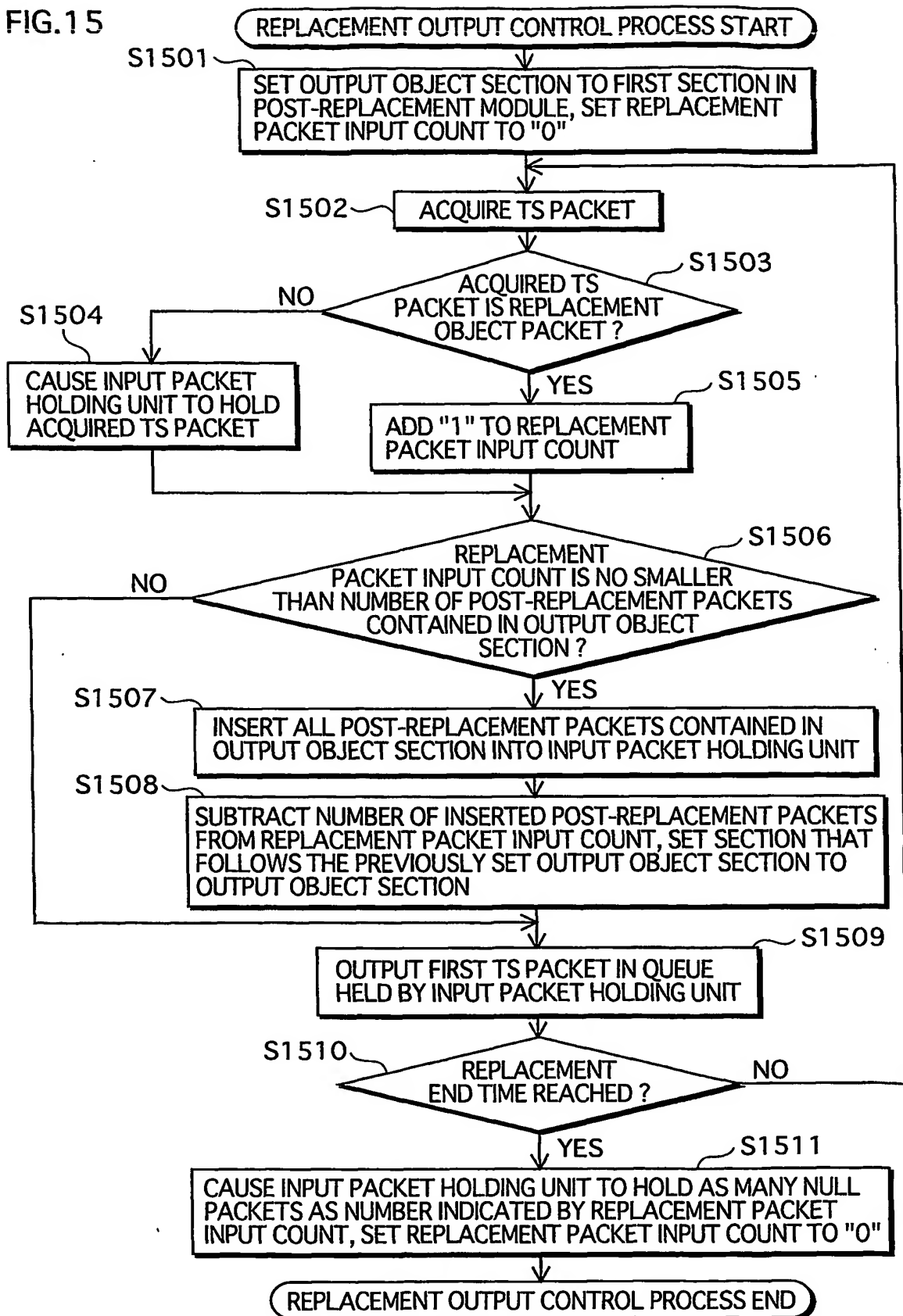


FIG.16

INPUT TS PACKET STREAM 71

1601	1602	1603	1604	1605	1606	1607	1608	1609	1610	1611	1612	1613
PID=100 mid=0 snum=1 bnum=0 pnum=2		PID=100 mid=1 snum=2 bnum=0 pnum=4	PID=100		PID=100	PID=100 mid=1 snum=2 bnum=1 pnum=2	PID=100 mid=0 snum=1 bnum=0 pnum=2	PID=100 mid=0 snum=1 bnum=0 pnum=2	PID=100 mid=1 snum=2 bnum=0 pnum=4	PID=100	PID=100	PID=100

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1651	1652	1653	1654	1655	1656	1657	1658	1659	1660	1661	1662	1663	1664	1665	1666
*	*	*	*	PID=100 mid=1 snum=2 bnum=0 pnum=4	PID=100	PID=100	PID=100	PID=100 mid=0 snum=1 bnum=0 pnum=3	PID=100 mid=1 snum=2 bnum=1 pnum=1	PID=100	PID=100 mid=1 snum=2 bnum=1 pnum=1	PID=100 mid=1 snum=2 bnum=0 pnum=4	PID=100	PID=100	PID=100

... .

OUTPUT TS PACKET STREAM 72

TIME

FIG. 17A

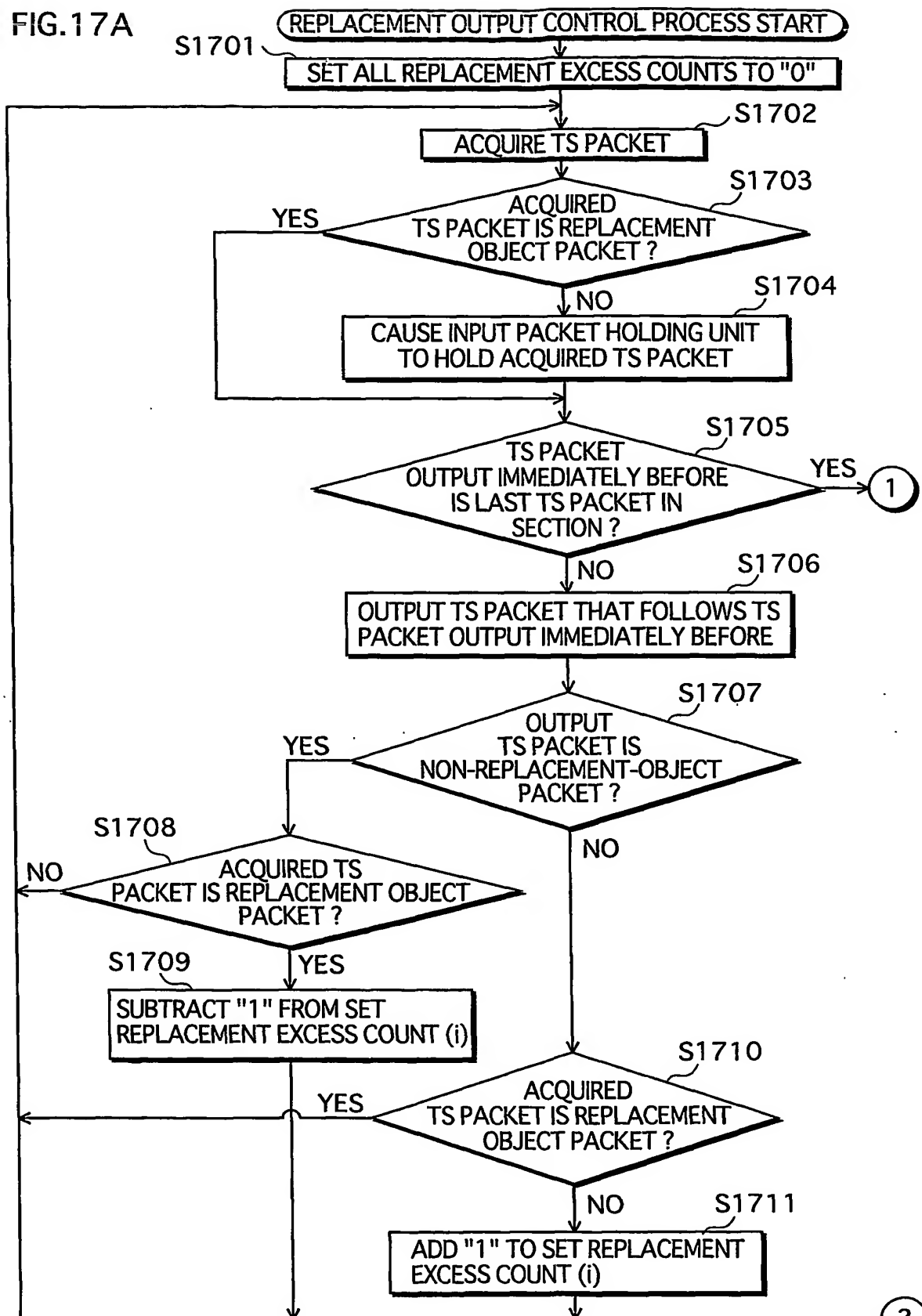


FIG.17B

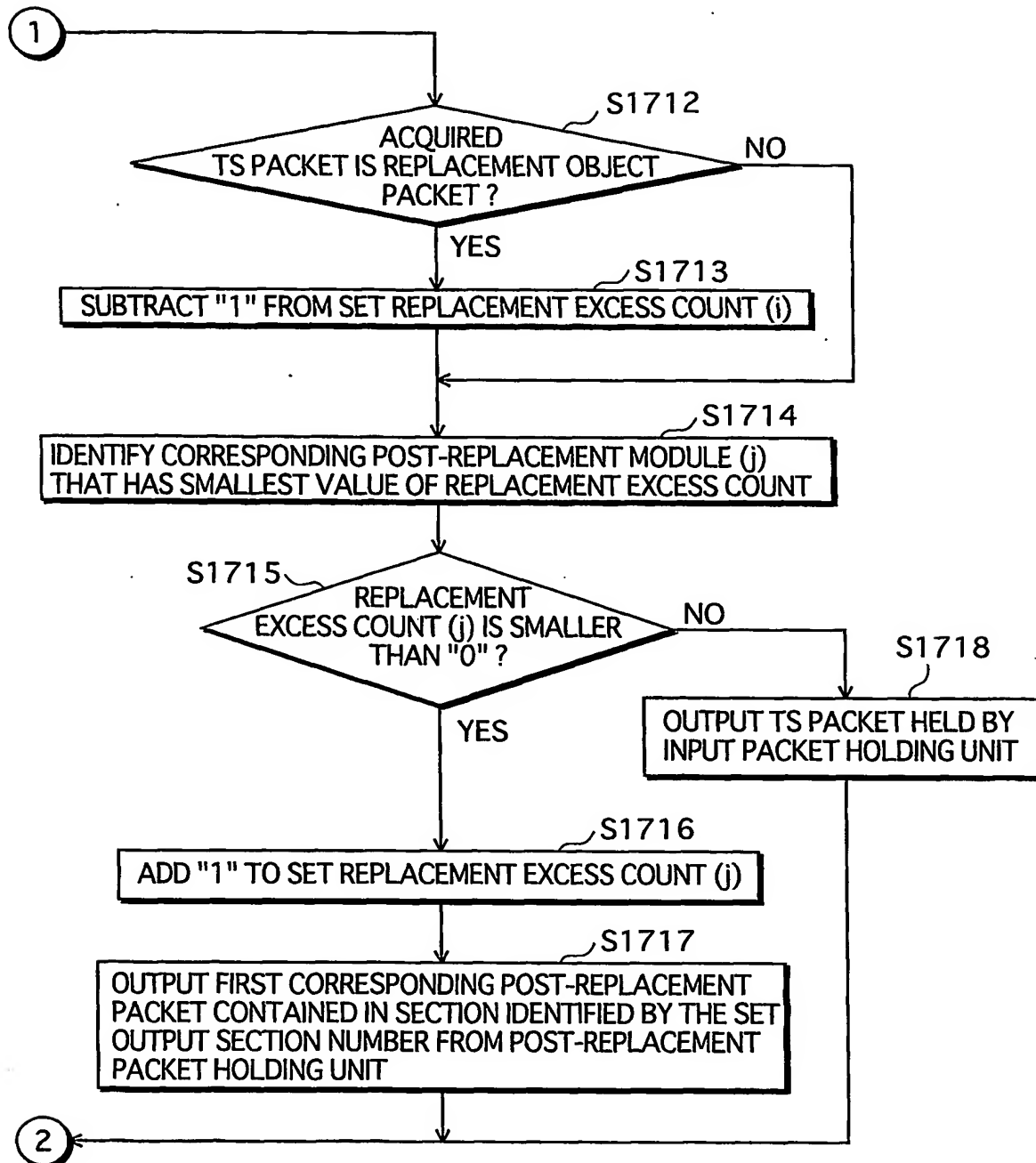


FIG.18

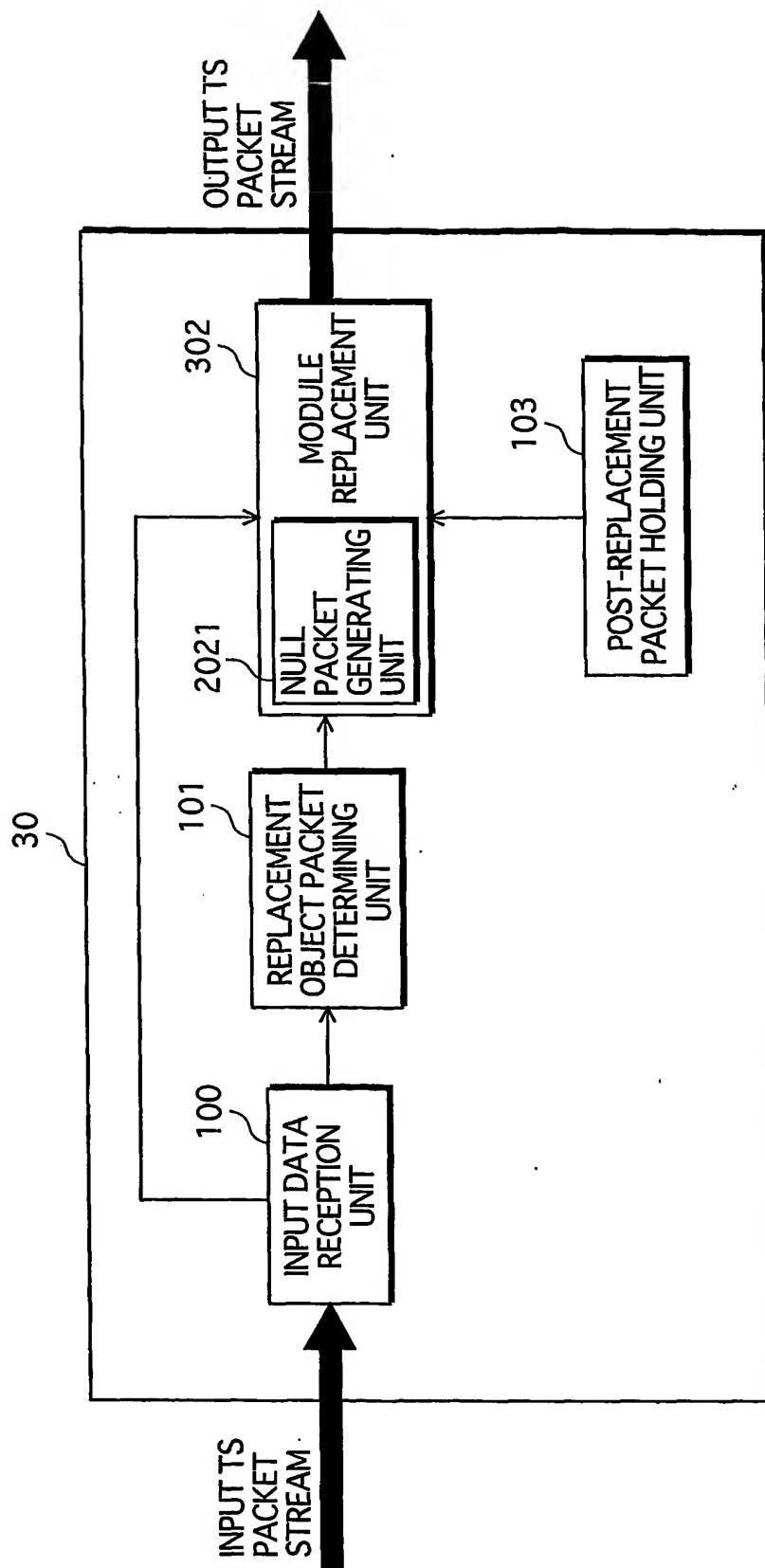


FIG. 19

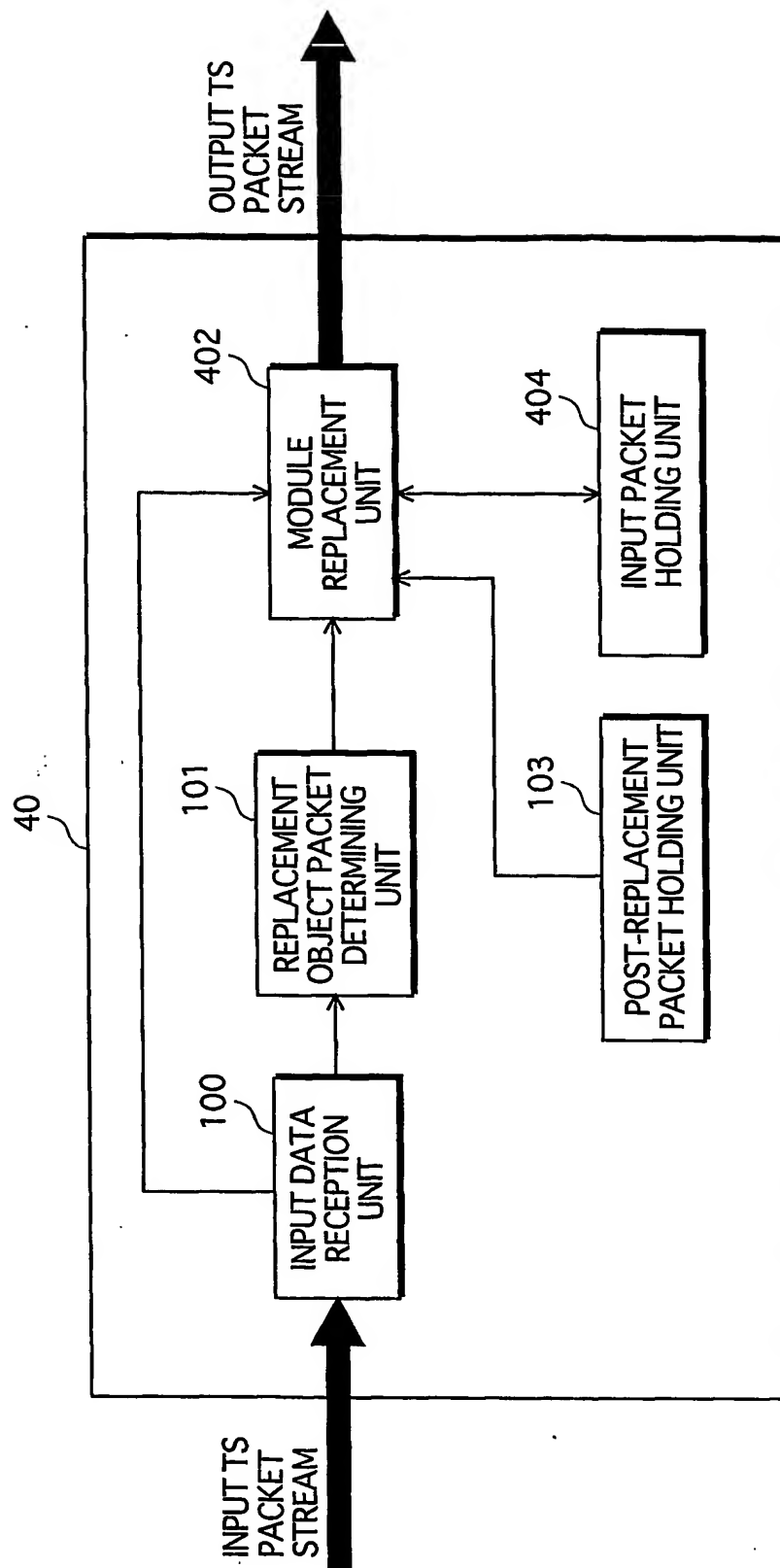


FIG.20

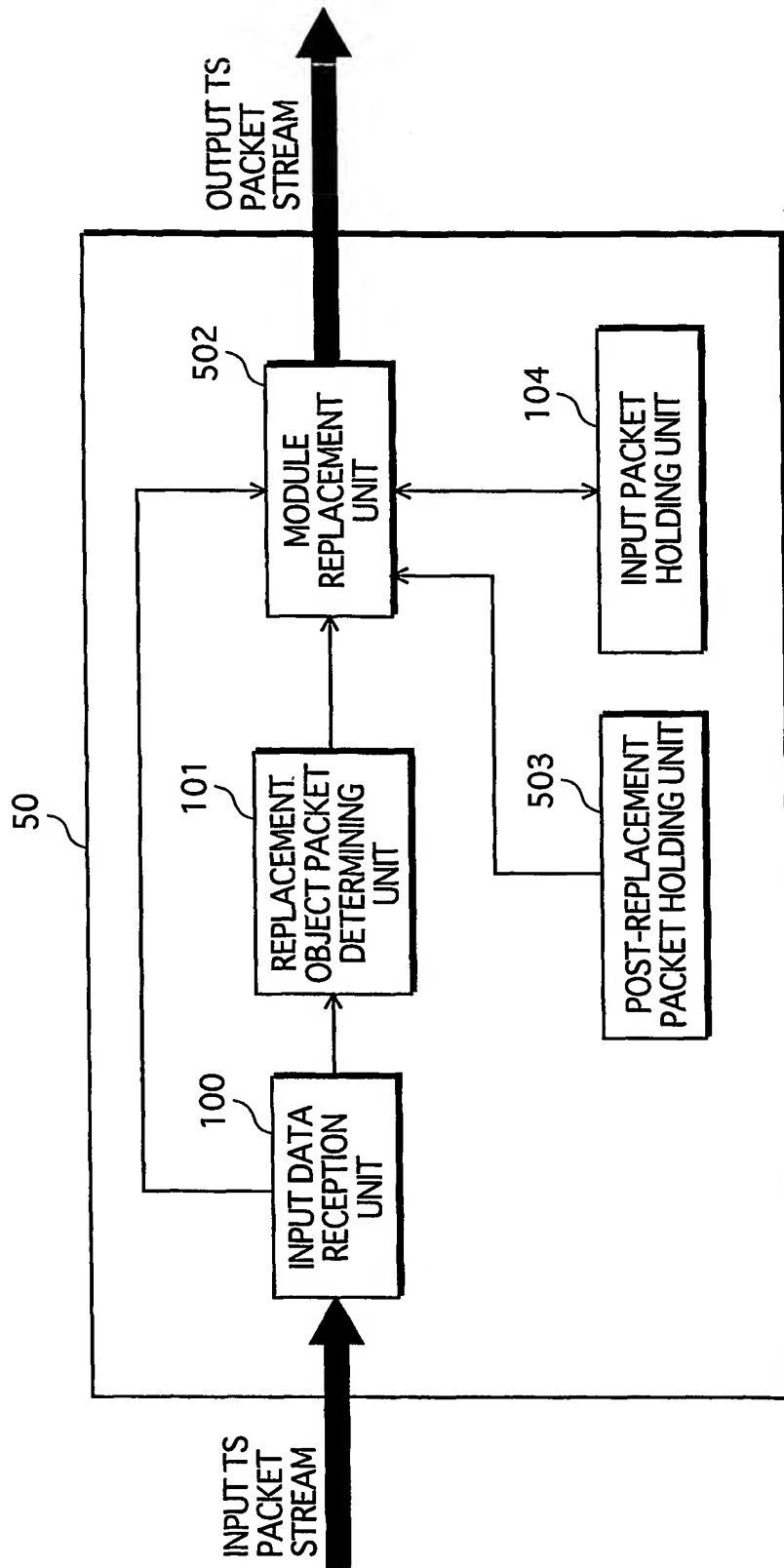


FIG.21A

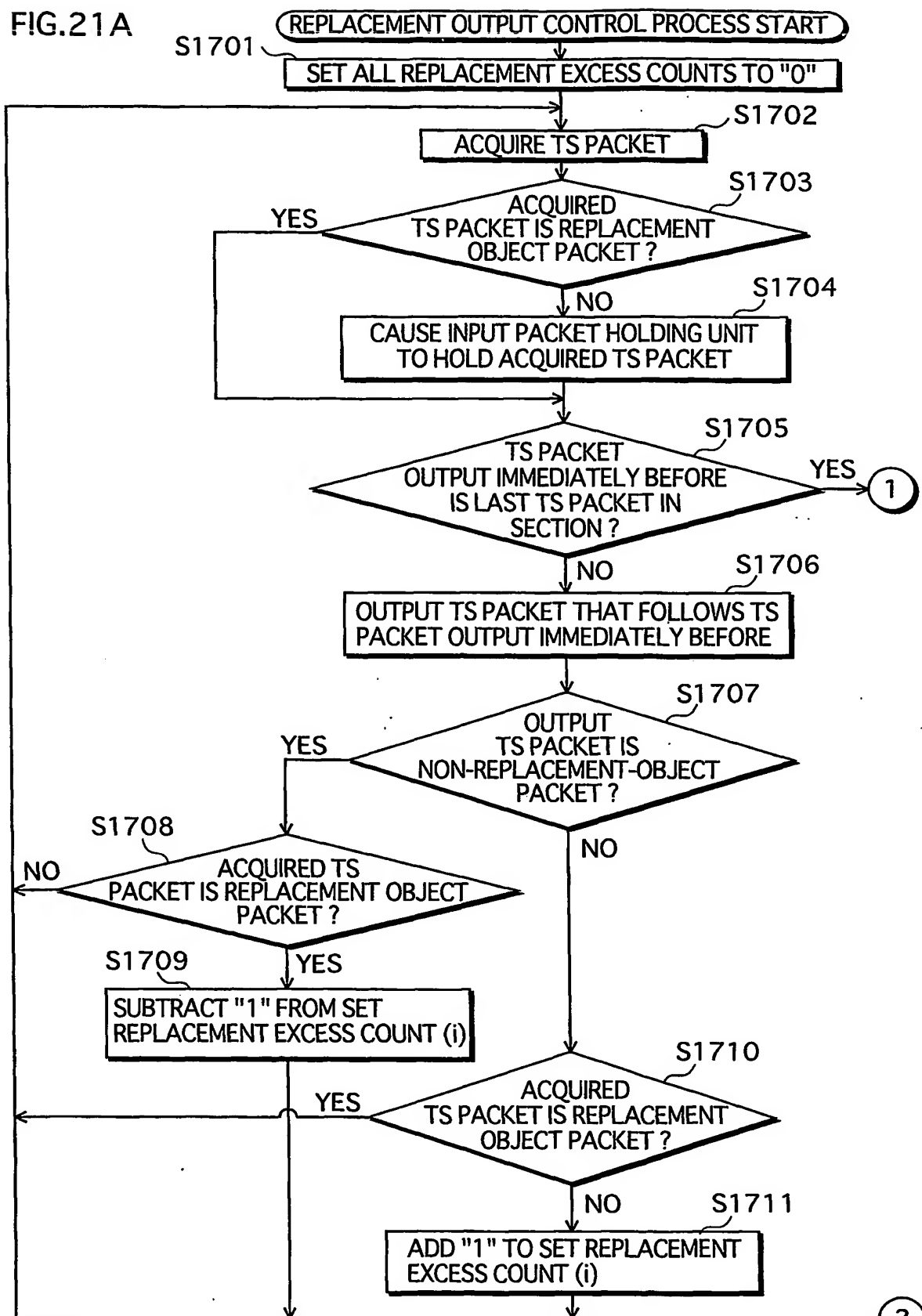


FIG.21B

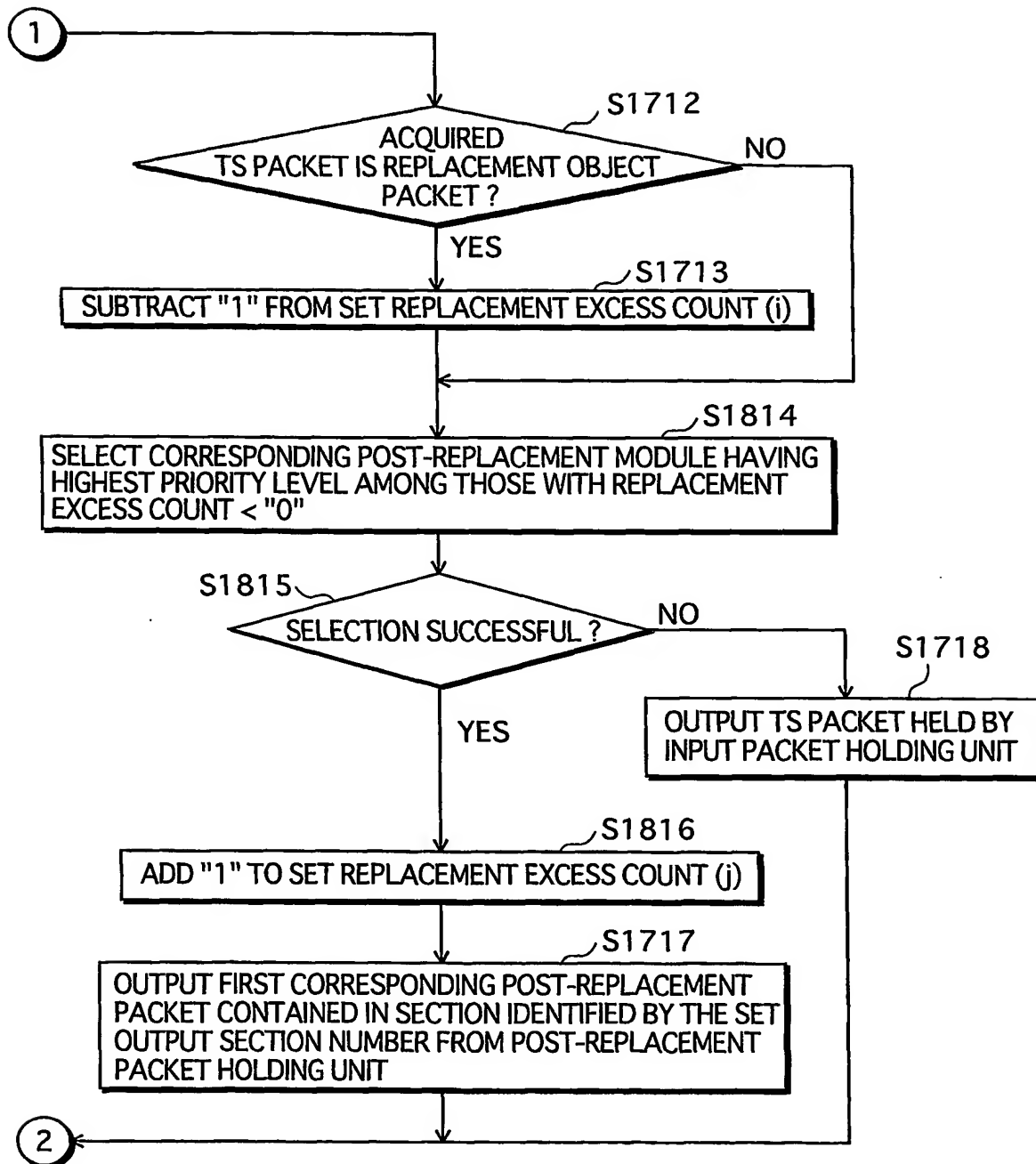


FIG.22A

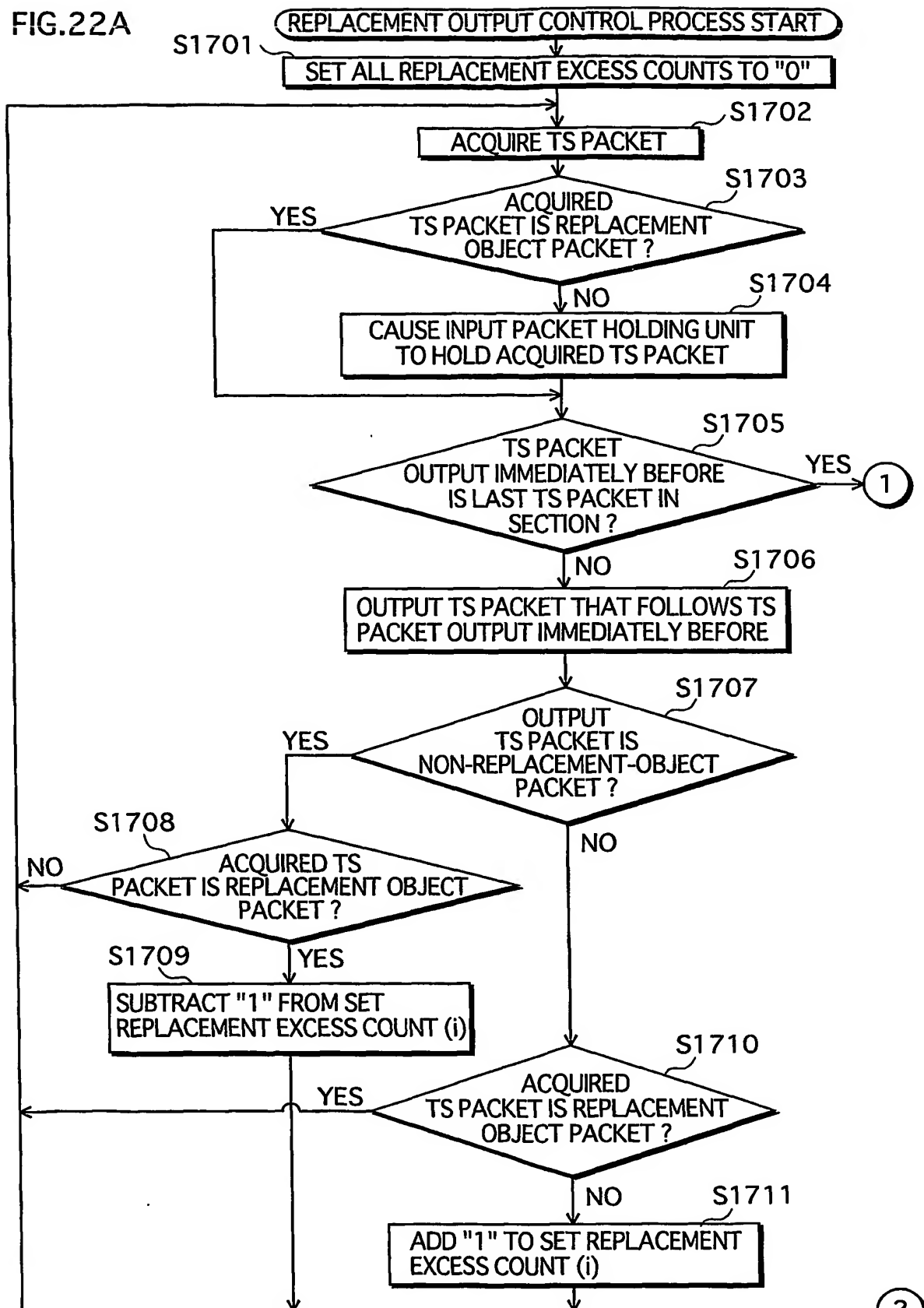


FIG.22B

